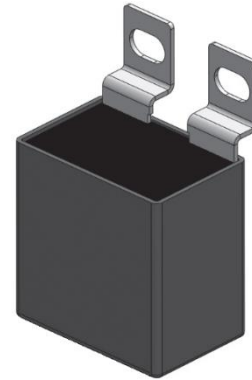
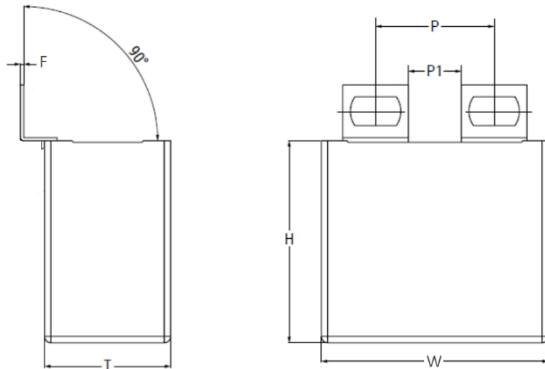


IGBT Snubber Capacitors (Direct Mounting)

■ 外形圖 Outline Drawing (For Example)



■ 典型應用 Typical Applications

這些電容器用於高壓、大電流以及高脈衝應用，例如：

IGBT 保護電路和緩衝網絡

電力電子中的能量轉換與控制

開關電源中的保護電路

These capacitors are used in high voltage, high current

And high pulse applications such as:

IGBT protection circuits & Snubber networks

Energy conversion and control in power electronics

Protection circuits in SMPS

■ 特徵 Features

延長的雙面金屬化聚酯電極

採用金屬化聚丙烯電介質內部串聯連接

塑料外殼 (UL94V-0) · 環氧樹脂密封

鍍錫黃銅接線片直接安裝 IGBT

Extended double metallised polyester electrodes

with metallised polypropylene dielectric internal series connection

UL 94 V-0 plastic case with thermosetting resin-fill

It has a tinned brass lug direct IGBT mounting

■ 規格 Specifications

參考標準 Reference Standard	GB/T 17702 (IEC 61071)				
氣候類別 Climatic Category	40/85/56				
最大允許外殼溫度 (T_{case}) Maximum permissible case temperature (T_{case})	-40°C ~ +85°C				
容值範圍 Capacitance Range	0.047 μ F ~ 9 μ F				
額定電壓 Rated Voltage (U_N)	630Vdc	700Vdc	850Vdc	1000Vdc	1200Vdc
	1600Vdc	1700Vdc	2000Vdc	2500Vdc	3000Vdc
容值公差 Capacitance Tolerance	$\pm 5\%$ (J) 、 $\pm 10\%$ (K)				
損耗因素 Dissipation Factor	$\leq 10 \times 10^{-4}$ at 1kHz, 1Vrms				
絕緣電阻 Insulation Resistance	$C_N \leq 0.33\mu F$, $IR \geq 15\ 000M\Omega$				25°C, 100Vdc, 60 seconds
	$C_N > 0.33\mu F$, $IR \times C_R \geq 5\ 000s$				

IGBT Snubber Capacitors (Direct Mounting)

■ 電氣特性 Electrical Characteristics

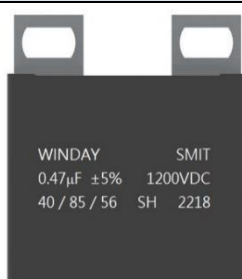
端子間耐受電壓 Withstanding voltage (V_{TT})	1.5 x U_{NDC} for 10 s, cut off current 10 mA		
絕緣電阻 Insulation Resistance ($IR \times C_N$)	$C_N \leq 0.33\mu F$, $IR \geq 15\ 000M\Omega$ $C_N > 0.33\mu F$, $IR \times C_R \geq 5\ 000s$ (25°C, 100Vdc, 60 seconds)		
浪湧電壓 Surge Voltage	1.5 * V_{NDC} for maximum 10 times in lifetime at T = 25°C ±5°C		
過電壓 Over voltage	1.1 x U_N	有負荷時間的 30%	一天內最長持續時間 Maximum duration within one day
	1.15 x U_N	30 分鐘	
	1.2 x U_N	5 分鐘	
	1.3 x U_N	1 分鐘	
自感 Self Inductance (L_S)	< 1nH per mm of lead spacing)		
最大峰值電流 Maximum peak current ↑ (A)	↑ = C x dV/dt		

■ 產品代碼構成 Product code system (For Example)

SMIT	I	474	J	1200	D	2	23	DP
型號 Type	內部使用 Internal use	容值 Capacitance	公差 Tolerance	額定電壓 Rated Voltage	交直流 AC/DC	端片樣式 Terminals Style	孔的距離 Distance of hole	內部使用 Internal use
SMIT=	--	474	J=±5%	0630=630V	D=DC	Shown as	23=23mm	--
IGBT		=470nF	K=±10%	0700=700V		Table I (表一)	26=26mm	
Snubber		=0.47μF		0850=850V			35=35mm	
Capacitors				1000=1000V			40=40mm	
(Boxed)				更多 More...				

■ 標示 Mark (For Example)

Marking



1. Manufacturer's name: WINDAY	2. SMIT: Type Construction
3. Capacitance: 0.47μF	4. Capacitors Tolerance: ±5%
5. Rated Voltage: 1200VDC	6. Climatic Category: 40/85/56
7. Self-Healing in nature such as SH	8. Date Code : 2218, Years = 2022, Weeks = 18

■ 尺寸 Dimensions (mm)

630Vdc/700Vdc (420Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR	L _S	I _{max}	Part number
				(V/us)	(A)	@100kHz (mΩ)	(nH)	@100kHz (A)	
0.68	37	25	15	900	612	5.0	23	9	SMIT_684+0630D*##DP
1.0	37	30	16	900	900	5.0	23	12	SMIT_105+0630D*##DP
1.2	37	30	16	900	1080	4.5	23	14	SMIT_125+0630D*##DP
1.5	37	34	20	900	1350	4.5	23	17	SMIT_155+0630D*##DP
1.8	37	34	20	900	1620	4.5	23	18	SMIT_185+0630D*##DP
2.0	42	40	20	600	1200	4.0	29	18	SMIT_205+0630D*##DP
2.2	42	40	20	600	1320	4.0	29	18.5	SMIT_225+0630D*##DP
2.5	42	40	20	600	1500	4.0	29	19	SMIT_255+0630D*##DP
3.0	42	44	24	600	1800	4.0	29	20	SMIT_305+0630D*##DP
3.3	42	44	24	600	1980	3.5	29	20	SMIT_335+0630D*##DP
4.0	42	44	24	600	2400	3.5	29	21	SMIT_405+0630D*##DP
4.7	42	45	30	600	2820	3.5	29	23	SMIT_475+0630D*##DP
5.0	42	45	30	600	3000	3.0	29	23.5	SMIT_505+0630D*##DP
6.0	42	43	42	600	3600	3.0	29	25	SMIT_605+0630D*##DP
6.5	42	43	42	600	3900	3.0	29	26	SMIT_655+0630D*##DP
6.5	57	45	30	360	2340	2.5	33	24	SMIT_655+0630D*##DP
7.0	57	45	30	360	2520	2.5	33	25	SMIT_705+0630D*##DP
8.0	57	50	35	360	2880	2.5	33	27	SMIT_805+0630D*##DP
9.0	57	50	35	360	3240	2.5	33	29	SMIT_905+0630D*##DP

Notes

- (1) The symbol + means capacitance tolerance (J=±5%, K=±10%)
- (2) The symbol * means style of solder slice
- (3) The symbol ## means distance of hole
- (4) Rated voltage pulse slope (dV/dt) at voltage U_{NDC}
- (5) Maximum RMS current at 100 kHz, θ_{amb}=70 °C (cooling-air temperature), Δθ_{case}=15°C (container temperature rise)
- (6) Equivalent series resistance and L_S are typical values at f = 100 kHz

■ 尺寸 Dimensions (mm)

850Vdc (450Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR @100kHz	L _S	I _{max} @100kHz	Part number
				(V/us)	(A)	(mΩ)	(nH)	(A)	
0.47	37	25	15	1200	564	5.0	23	9	SMIT_474+0850D*##DP
0.68	37	30	16	1200	816	5.0	23	12	SMIT_684+0850D*##DP
1.0	37	34	20	1200	1200	5.0	23	14	SMIT_105+0850D*##DP
1.2	37	34	20	1200	1440	5.0	23	16	SMIT_125+0850D*##DP
1.5	37	34	20	1200	1880	5.0	23	18	SMIT_155+0850D*##DP
1.5	42	40	20	750	1125	4.5	29	18.5	SMIT_155+0850D*##DP
2.0	42	40	20	750	1500	4.5	29	19	SMIT_205+0850D*##DP
2.2	42	40	20	750	1650	4.5	29	19.5	SMIT_225+0850D*##DP
2.5	42	44	24	750	1875	4.5	29	20	SMIT_255+0850D*##DP
3.0	42	44	24	750	2250	4.5	29	21	SMIT_305+0850D*##DP
3.3	42	45	30	750	2475	4.5	29	21.5	SMIT_335+0850D*##DP
4.0	42	43	42	750	3000	4.5	29	22	SMIT_405+0850D*##DP
4.0	57	45	30	450	1800	4.0	33	23	SMIT_405+0850D*##DP
4.7	57	45	30	450	2115	4.0	33	24.5	SMIT_475+0850D*##DP
5.0	57	45	30	450	2250	4.0	33	25	SMIT_505+0850D*##DP
6.0	57	50	35	450	2700	4.0	33	26	SMIT_605+0850D*##DP
6.5	57	50	35	450	2925	4.0	33	27	SMIT_655+0850D*##DP

Notes

- (1) The symbol + means capacitance tolerance (J=±5%, K=±10%)
- (2) The symbol * means style of solder slice
- (3) The symbol ## means distance of hole
- (4) Rated voltage pulse slope (dV/dt) at voltage U_{NDC}
- (5) Maximum RMS current at 100 kHz, $\theta_{amb}=70\text{ }^{\circ}\text{C}$ (cooling-air temperature), $\Delta\theta_{case}=15\text{ }^{\circ}\text{C}$ (container temperature rise)
- (6) Equivalent series resistance and L_S are typical values at f = 100 kHz

■ 尺寸 Dimensions (mm)

1000Vdc (500Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR	L _S	I _{max}	Part number
				(V/us)	(A)	@100kHz (mΩ)	(nH)	@100kHz (A)	
0.47	37	25	15	1300	611	5.0	23	9	SMIT_474+1000D*##DP
0.68	37	30	16	1300	884	5.0	23	10.5	SMIT_684+1000D*##DP
0.82	37	30	16	1300	1066	5.0	23	12	SMIT_824+1000D*##DP
1.0	37	34	20	1300	1300	4.5	23	15	SMIT_105+1000D*##DP
1.2	37	34	20	1300	1560	4.5	23	17	SMIT_125+1000D*##DP
1.2	42	40	20	850	1020	4.5	29	16	SMIT_125+1000D*##DP
1.5	42	40	20	850	1275	4.5	29	16	SMIT_155+1000D*##DP
2.0	42	44	24	850	1700	4.5	29	17	SMIT_205+1000D*##DP
2.2	42	44	24	850	1870	4.0	29	20	SMIT_225+1000D*##DP
2.5	42	45	30	850	2125	4.0	29	21	SMIT_255+1000D*##DP
3.0	42	45	30	850	2550	4.0	29	21.5	SMIT_305+1000D*##DP
3.3	42	43	42	850	2805	4.0	29	22	SMIT_335+1000D*##DP
3.3	57	45	30	500	1650	4.0	33	20	SMIT_335+1000D*##DP
4.0	57	45	30	500	2000	4.0	33	21	SMIT_405+1000D*##DP
4.7	57	50	35	500	2350	4.0	33	22	SMIT_475+1000D*##DP
5.0	57	50	35	500	2500	4.0	33	23	SMIT_505+1000D*##DP

Notes

- (1) The symbol + means capacitance tolerance (J=±5%, K=±10%)
- (2) The symbol * means style of solder slice
- (3) The symbol ## means distance of hole
- (4) Rated voltage pulse slope (dV/dt) at voltage U_{NDC}
- (5) Maximum RMS current at 100 kHz, θ_{amb}=70 °C (cooling-air temperature), Δθ_{case}=15°C (container temperature rise)
- (6) Equivalent series resistance and L_S are typical values at f = 100 kHz

■ 尺寸 Dimensions (mm)

1200Vdc (600Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR @100kHz	L _S	I _{max} @100kHz	Part number
				(V/us)	(A)	(mΩ)	(nH)	(A)	
0.33	37	25	15	1500	495	4.5	23	9	SMIT_334+1200D*##DP
0.47	37	30	16	1500	705	4.5	23	11	SMIT_474+1200D*##DP
0.68	37	34	20	1500	1020	4.5	23	12.5	SMIT_684+1200D*##DP
0.75	37	34	20	1500	1125	4.5	23	13	SMIT_754+1200D*##DP
0.82	42	40	20	950	779	4.0	29	14.5	SMIT_824+1200D*##DP
1.0	42	40	20	950	950	4.0	29	16	SMIT_105+1200D*##DP
1.2	42	44	24	950	1140	4.0	29	19	SMIT_125+1200D*##DP
1.5	42	44	24	950	1425	4.0	29	19.5	SMIT_155+1200D*##DP
2.0	42	45	30	950	1900	4.0	29	20	SMIT_205+1200D*##DP
2.2	42	43	42	950	2090	4.0	29	21	SMIT_225+1200D*##DP
2.5	42	43	42	950	2375	4.0	29	22	SMIT_255+1200D*##DP
2.2	57	45	30	600	1320	3.8	33	20	SMIT_225+1200D*##DP
2.5	57	45	30	600	1500	3.8	33	21	SMIT_255+1200D*##DP
3.0	57	45	30	600	1800	3.8	33	22	SMIT_305+1200D*##DP
3.3	57	50	35	600	1980	3.8	33	23	SMIT_335+1200D*##DP
4.0	57	50	35	600	2400	3.8	33	24	SMIT_405+1200D*##DP

Notes

- (1) The symbol + means capacitance tolerance (J=±5%, K=±10%)
- (2) The symbol * means style of solder slice
- (3) The symbol ## means distance of hole
- (4) Rated voltage pulse slope (dV/dt) at voltage U_{NDC}
- (5) Maximum RMS current at 100 kHz, θ_{amb}=70 °C (cooling-air temperature), Δθ_{case}=15°C (container temperature rise)
- (6) Equivalent series resistance and L_S are typical values at f = 100 kHz

■ 尺寸 Dimensions (mm)

1600Vdc (650Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR	L _S	I _{max}	Part number
				(V/us)	(A)	@100kHz (mΩ)	(nH)	@100kHz (A)	
0.22	37	25	15	1900	418	6.0	23	8	SMIT_224+1600D*##DP
0.33	37	30	16	1900	627	6.0	23	10	SMIT_334+1600D*##DP
0.39	37	34	20	1900	741	5.5	23	12	SMIT_394+1600D*##DP
0.47	37	34	20	1900	893	5.5	23	14	SMIT_474+1600D*##DP
0.68	42	40	20	1250	850	4.0	29	16	SMIT_684+1600D*##DP
0.82	42	44	24	1250	1025	4.0	29	19	SMIT_824+1600D*##DP
1.0	42	45	30	1250	1250	4.0	29	19.5	SMIT_105+1600D*##DP
1.2	42	45	30	1250	1500	4.0	29	20	SMIT_125+1600D*##DP
1.5	42	43	42	1250	1875	4.0	29	21	SMIT_155+1600D*##DP
1.5	57	45	30	750	1125	3.5	33	22	SMIT_155+1600D*##DP
2.0	57	50	35	750	1500	3.5	33	24	SMIT_205+1600D*##DP

Notes

- (1) The symbol + means capacitance tolerance (J=±5%, K=±10%)
- (2) The symbol * means style of solder slice
- (3) The symbol ## means distance of hole
- (4) Rated voltage pulse slope (dV/dt) at voltage U_{NDC}
- (5) Maximum RMS current at 100 kHz, $\theta_{amb}=70^{\circ}\text{C}$ (cooling-air temperature), $\Delta\theta_{case}=15^{\circ}\text{C}$ (container temperature rise)
- (6) Equivalent series resistance and L_S are typical values at f = 100 kHz

■ 尺寸 Dimensions (mm)

1700Vdc (675Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR	L _S	I _{max}	Part number
				(V/us)	(A)	@100kHz (mΩ)	(nH)	@100kHz (A)	
0.15	37	25	15	2000	300	7.0	23	7	SMIT_154+1700D*##DP
0.22	37	30	16	2000	440	6.0	23	9	SMIT_224+1700D*##DP
0.33	37	34	20	2000	660	5.5	23	11.5	SMIT_334+1700D*##DP
0.39	37	34	20	2000	780	5.5	23	13	SMIT_394+1700D*##DP
0.47	42	36	24	1260	592	4.0	29	14	SMIT_474+1700D*##DP
0.56	42	36	24	1260	706	4.0	29	15.5	SMIT_564+1700D*##DP
0.68	42	44	24	1260	857	3.5	29	18	SMIT_684+1700D*##DP
0.82	42	44	24	1260	1033	3.5	29	19	SMIT_824+1700D*##DP
1.0	42	45	30	1260	1260	3.5	29	20	SMIT_105+1700D*##DP
1.2	42	43	42	1260	1512	3.5	29	21	SMIT_125+1700D*##DP
1.0	57	45	25	780	780	3.5	33	18	SMIT_105+1700D*##DP
1.2	57	43.5	29.5	780	936	3.5	33	19	SMIT_125+1700D*##DP
1.5	57	50	35	780	1170	3.0	33	22	SMIT_155+1700D*##DP
2.0	57	50	35	780	1560	3.0	33	24	SMIT_205+1700D*##DP
3.0	57	55	45	780	2340	3.0	33	28	SMIT_305+1700D*##DP

Notes

- (1) The symbol + means capacitance tolerance (J=±5%, K=±10%)
- (2) The symbol * means style of solder slice
- (3) The symbol ## means distance of hole
- (4) Rated voltage pulse slope (dV/dt) at voltage U_{NDC}
- (5) Maximum RMS current at 100 kHz, θ_{amb}=70 °C (cooling-air temperature), Δθ_{case}=15°C (container temperature rise)
- (6) Equivalent series resistance and L_S are typical values at f = 100 kHz

■ 尺寸 Dimensions (mm)

2000Vdc (700Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR @100kHz	L _S	I _{max} @100kHz	Part number
				(V/us)	(A)	(mΩ)	(nH)	(A)	
0.10	37	25	15	2241	224	8.0	23	7	SMIT_104+2000D*##DP
0.15	37	25	15	2241	336	8.0	23	8.5	SMIT_154+2000D*##DP
0.22	37	30	16	2241	493	6.0	23	10	SMIT_224+2000D*##DP
0.33	37	34	20	2241	740	6.0	23	13	SMIT_334+2000D*##DP
0.47	42	40	20	1300	611	4.0	29	15.5	SMIT_474+2000D*##DP
0.56	42	44	24	1300	728	4.0	29	18	SMIT_564+2000D*##DP
0.68	42	44	24	1300	884	3.5	29	18.5	SMIT_684+2000D*##DP
0.82	42	45	30	1300	1066	3.5	29	19	SMIT_824+2000D*##DP
1.0	42	43	42	1300	1300	3.5	29	21	SMIT_105+2000D*##DP
1.0	57	45	30	850	850	4.0	33	24	SMIT_105+2000D*##DP
1.2	57	45	30	850	1020	4.0	33	23	SMIT_125+2000D*##DP
1.5	57	50	35	850	1275	4.0	33	24	SMIT_155+2000D*##DP

Notes

- (1) The symbol + means capacitance tolerance (J=±5%, K=±10%)
- (2) The symbol * means style of solder slice
- (3) The symbol ## means distance of hole
- (4) Rated voltage pulse slope (dV/dt) at voltage U_{NDC}
- (5) Maximum RMS current at 100 kHz, θ_{amb}=70 °C (cooling-air temperature), Δθ_{case}=15°C (container temperature rise)
- (6) Equivalent series resistance and L_S are typical values at f = 100 kHz

IGBT Snubber Capacitors (Direct Mounting)

■ 尺寸 Dimensions (mm)

2500Vdc (725Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR @100kHz	L _S	I _{max} @100kHz	Part number
				(V/us)	(A)	(mΩ)	(nH)	(A)	
0.068	37	25	15	3230	220	8.5	23	6.5	SMIT_683+2500D*##DP
0.10	37	30	16	3230	323	8.5	23	8	SMIT_104+2500D*##DP
0.15	37	34	20	3230	485	8.0	23	11	SMIT_154+2500D*##DP
0.18	37	34	20	3230	581	7.5	23	12.5	SMIT_184+2500D*##DP
0.22	42	40	20	2100	462	4.0	29	14	SMIT_224+2500D*##DP
0.33	42	44	24	2100	693	4.0	29	15.5	SMIT_334+2500D*##DP
0.47	42	45	30	2100	987	3.5	29	18	SMIT_474+2500D*##DP
0.68	42	43	42	2100	1428	3.5	29	18.5	SMIT_684+2500D*##DP
0.68	57	45	30	1200	816	3.5	33	19	SMIT_684+2500D*##DP
1.0	57	50	35	1200	1200	3.5	33	19.5	SMIT_105+2500D*##DP

3000Vdc (750Vac)									
Cap. μF	W _{±1.0}	H _{±1.0}	T _{±1.0}	dV/dt	I _{PEAK}	ESR @100kHz	L _S	I _{max} @100kHz	Part number
				(V/us)	(A)	(mΩ)	(nH)	(A)	
0.047	37	25	15	3361	158	8.5	23	7.5	SMIT_473+3000D*##DP
0.068	37	30	16	3361	229	8.0	23	9	SMIT_683+3000D*##DP
0.10	37	34	20	3361	336	7.5	23	10.5	SMIT_104+3000D*##DP
0.15	37	34	20	3361	504	7.0	23	12	SMIT_154+3000D*##DP
0.22	42	40	20	2050	451	5.0	29	13	SMIT_224+3000D*##DP
0.33	42	45	30	2050	677	4.5	29	16.5	SMIT_334+3000D*##DP
0.47	42	43	42	2050	964	4.0	29	18	SMIT_474+3000D*##DP
0.47	42	45	30	1200	564	4.0	33	18.5	SMIT_474+3000D*##DP
0.68	57	50	35	1200	816	4.0	33	19	SMIT_684+3000D*##DP
0.82	57	50	35	1200	984	3.5	33	20	SMIT_824+3000D*##DP